Mark Paulik, Ph.D.
Curriculum Vitae

Professor and Director
Robotics and Mechatronic Systems Engineering
Electrical and Computer Engineering Dept.
University of Detroit Mercy
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Linden, MI 48451-9604
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PROFILE

Experienced in leadership, academic administration, faculty mentoring, recruitment, ABET accreditation, teaching, research, and grant writing. E□ective with industrial and government research partners. United States Citizen.

EDUCATION

Ph.D., Systems/Electrical Engineering (ECE: 2D Signal/Image Processing) June 1989
Oakland University; Rochester, Michigan

Science Masters, Electrical Engineering (Embedded Systems) June 1983
Massachusetts Institute of Technology; Cambridge, MA

Bachelor of Electrical Engineering May 1981
University of Detroit; Detroit, Michigan

PROFESSIONAL EXPERIENCE

Professor of Electrical and Computer Engineering **August 2001 - Present**
University of Detroit Mercy (UDM)

Chairperson, Department of Electrical and Computer Engineering, August 2015 - 2016
August 2007 - 2010

- Prepared ABET self-study documents for the Electrical Engineering program. Team visit: October 2016 (successful)
- Prepared ABET self-study documents for the new Robotics and Mechatronic Systems/Electrical Engineering program. Team visit: October 2016 (successful)
- Negotiated the addition of one new tenure-track faculty line (search underway) and two new instructor faculty lines (hired August 2016)
- Wrote the self-study and led the department through a successful ABET accreditation in 2010.
- Developed a 5-year Bachelors/Masters program in Robotics Engineering.
- Negotiated the addition of two new faculty lines and hired two new ECE faculty (2009)

Program Director, Robotics and Mechatronic Systems Engineering (new program launch),
University of Detroit Mercy, **2012 - Present**

- Coordinated curriculum design and development

- Assisted with the development and launch of a Robotics and Mechatronic Systems 3+2 program with multiple Chinese Universities. Program launch: Fall 2017.
- Developed marketing materials (website, banners, brochures, and giveaways) and personally

- 2012, 2013 — First and Second place in Joint Architecture for Unmanned Systems (JAUS) competition, 6th place Design.
- 2008, 2009, 2010 — Winner First Place Overall IGVC
- 2006, 2007 — Winner ird Place Overall IGVC
- 2005 — Sixth Place Autonomous Challenge Competition IGVC

Embedded Systems Laboratory

Advanced, Audio system, Control, and Robot-based projects using mobile or arm-based actuators in combination with multiple sensors and interface protocols.

Hardware Description Languages: VHDL:

Advanced treatment of digital system design methodology, VHDL design and simulation language (Structural, Data □ow, and Behavioral), Simulation and Synthesis construction and demonstration of FPGA based projects

Hardware Description Languages Design Laboratory

Implementation of FPGA-based system designs (e.g. video systems, mp3 encoders, roboT Q q 1 tg41dL -

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61. M. Das, M. J. Paulik, and N. K. Loh, "A Projection Based Constrained Optimization Technique for One Shot Optimal Design of Stable 1-D and Separable 2-D IIR Filters," Proceedings of the International Conference on Acoustics, Speech, & Signal Processing, Dallas Texas, April 1987.